GURU NANAK INSTITUTE OF TECHNOLOGY

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www.gnithyd.ac.in   Campus: Ibrahimpatnam, R.R. District, Hyderabad-501506, Telangana, India. Ph: (0/95) 8414-20 21 20/21

Registration Fee

Rs. 1000/- (for students)
Rs. 1200/- (for faculty , researchers)
Rs.1500/-   (Industry personnel)
              (Non- Refundable)
   Participants are limited to 60 and the registration will be on first come first serve basis.

REGISTRATION FORM:

Name:..
Designation:...
Organization:.......................................
Address:...
Email:...
Academic Qualification:......................,
Specialization:......................................
Experience:..

Signature of the Applicant with date

You can Pay your Registration fee Online by depositing the registration fee through NEFT/RTGS at the following authorized account:
Name: Guru Nanak Institute of Technology
Acc.No.:24101110000104
Bank:Andhra Bank
Branch:Khanapur
IFSC Code:ANDB0002410

Please send the completed applications, signed by the competent authority, along with registration fee to:

Correspondence Address:
Mr.I.Sharath Chandra,
Asst. Professor, ECE, Coordinator,
Guru Nanak Institute of Technology,
Ibrahimpatnam,
Hyderabad-501506 (Telangana).
Telephone: 9885391926.
E-mail: sharathchandrai.ecegnit@gniindia.org

Programme Committee:

Chief Patrons:
Sardar Tavinder Singh Kohli,
Chairman, GNI
Sardar Gagandeep Singh Kohli,
Vice-Chairman, GNI

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Conveners:
Dr. S. Sreenatha Reddy, Principal, GNIT
Dr.M.Narendra Kumar, Vice-Principal, GNIT

Co– Convener:
Prof.B.Kedarnath,
HOD-ECE, GNIT

Co-ordinator:
Mr.I.Sharath Chandra, Asst. Prof., ECE,GNIT

GURU NANAK INSTITUTE OF TECHNOLOGY

DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

A FOUR DAY NATIONAL LEVEL WORKSHOP ON "REAL TIME DSP SYSTEM DESIGN, CODING AND OPTIMIZATION USING TMS320C6713"

09-02-2016 to12-02-2016

GN GURU NANAK INSTITUTIONS

Engineering • Dental • MBA • MCA • Pharmacy • PGDM

Campus: Ibrahimpatnam, R.R.Dist., Hyderabad-501506.
WEB: www.gniindia.org

About the department of ECE:

The department of Electronics and Communication Engineering has established in the year 1999. It is offering UG and two PG programs. The student intake of the department is 120 for UG program and 18 each for PG programs. The department is headed by the Professors, Associate Professors who are experts in their respective disciplines. The department has highly modernized laboratories with sophisticated equipment, which improves the practical working competency in the students. The department aims at educating and training students with sound knowledge and awareness in the latest trends in Electronics and Communication Engineering.

About the Institution:

The Guru Nanak Institute of Technology (GNIT), Ibrahimpatnam is approved by AICTE, New Delhi and Affiliated to Jawaharlal Nehru Technological University, Hyderabad. GNIT is the part of Guru Nanak Institutions, Hyderabad and a premier institution in the country, spread over 57 acres of prime land developed into gate way of knowledge and inspiration.The Institution offers graduate and post graduate programs in 5 disciplines of Engineering. The institution aims at inculcating technological competence, research capabilities, managerial skills and exemplary professional conduct through strong global academics, discipline, in our students.

Program Objective:

This program is designed keeping market requirements with intense conceptual and hands-on training program for students, faculty and researchers who want to strengthen their DSP software development skills.

Program Expected Outcomes :

• An exposure to the Industry Standards Tool.

• Get experienced with TI series Processor.

• To opens up new ideas in project implementation.

• To have a strong knowledge about the subject and their applications.

• To have solid knowledge of software/hardware debugging.

Detailed program schedule for the entire four days along with the course content will be provided at the time of registration.

Specific Outcomes aimed:

• Gain strong fundamentals in TMS320C6000 architecture and understand TI's industry standard Code Composer studio (CCS).

• Write optimized DSP algorithm in C , ASM and embedding C to ASM.

• Designing the DSP solution with efficient C and ASM code on CCS for simulator and the real time DSP platform (C6000).

• Implement various C6000 optimization techniques.

• Debug and profile in CCS.

• SP BIOS for Real Time Scheduling and Interrupt Configuration Optimization methods (with emphasis on Software Pipeline).

Resource persons are Training Consultants of TATA ELEXSI and faculty from CDAC who has enough Training experience along with an exposure to execute projects .
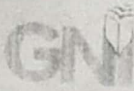
Guru Nanak Institute of Technology

[Approved by All India Council for Technical Education, New Delhi & Affiliated to JNTU, Hyderabad]

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[handwritten notes across top: "3 Days", "@ Rs 100/- /day", "3 Participants", "expenditure", "9.1/17"]

DEPARTMENT OF ELECTRONICS AND COMMUNICTION ENGINEERING
DT:02-01-2017

NOTE PUT UP TO THE PRINCIPAL
(THROUGH VICE PRINCIPAL)

On behalf of Department of ECE, GNIT in association with IETE Students Forum and ECE Department Association, we would like to organize a Three day National level workshop on **"Real time DSP System Design, Coding and Implementation on TMS320C6713 DSK"** from **23-01-2017** to **25-01-2017**, for which we have already got the necessary approval(s) previously through a note put up dated **06-01-2016**.

However, we could not conduct the workshop accordingly as per the plan, due to the announcement of dates of NBA inspection and non-availability of the resource person thereafter.

As, Students are studying this course in the current semester and as this workshop is purely hands-on and is dedicated to the lab sessions for all the three days, this will definitely help them to understand and implement the code on the DSP hardware.

We would like to bring a change to the previous proposal by limiting the number of participants to 30 as we have 10 DSP processor boards. so that we can use the resources to the maximum and create a good learning environment.

The **Estimated budget statement** and the **Programme Schedule** is attached for your reference.

We have also attached a copy of previous proposal which was approved for your kind perusal.

In this regard, we would like to request your permission and approval for organizing the same.

[handwritten: "HOD to discuss"]

Workshop Coordinator

HOD-ECE

[handwritten: "Forwarded & recommended for 3 days. 50% students from outside GNIT. 6/1/17"]

[handwritten: "May be organize workshop only ECE, Code Profit/able 3 days. 7.1.17"]

GURU NANAK INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

NATIONAL LEVEL WORKSHOP ON "REAL TIME DSP SYSTEM DESIGN, CODING AND IMPLEMENTATION ON TMS320C6713 DSK"

Estimated Budget:

Proposed Expenditure

S.No.	Name of the Item(s)	Amount
1	Remuneration for the Speaker and his travelling expenses	Rs. 19000
2	Workshop kits for registered participants and Other stationary	Rs. (3000) 1500
3	Certificates	Rs. 400
4	Banner (4'X 8') -- 3	Rs. 800
5	Working lunch, Tea and Snacks for Participants and Resource person	Rs. 7000
	Total Estimated Expenditure	Rs.(30,200) 28,700 (Thirty Thousand two hundred rupees only)

Expected Revenue

S.No.	Name of the Item(s)	Amount
1	Registration fee from the Participants (per participant-Rs.1000)	Rs. 30,000
	Total Expected Revenue	Rs.(30,000) .28,700 (Thirty Thousand rupees only)

*We also seek your permission for providing the accommodation for the resource person for four days in our college guest house i.e., from 22-01-2017 to 25-01-2017.

(Contd...)

GURU NANAK INSTITUTE OF TECHNOLOGY
DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING

NATIONAL LEVEL WORKSHOP ON "REAL TIME DSP SYSTEM DESIGN, CODING AND IMPLEMENTATION ON TMS320C6713 DSK"

Program Schedule

Venue: Basic Simulation lab, Dept. of ECE, 3rd Floor, GNIT.	
The same schedule will be followed for three days during the Workshop	
9:30 to 11:15 A.M	Session-1
11.15 to 11.30 A.M	Tea Break
11:30 A.M to 1:15 P.M	Session-2
1.15 P.M. to 2.00 P.M	Lunch
2.00 P.M to 4.00 P.M	Session-3

*During the Session-1 of the first day and Session-3 of the last day, we will be using 10 minutes for a short introduction of the guest and valedictory.

REPORT ON THREE DAY NATIONAL LEVEL WORKSHOP ON
"REAL TIME DSP SYSTEM DESIGN, CODING AND OPTIMIZATION USING TMS3206713 DSK"

JANUARY 23rd to 25th ,2017

The Department of ECE, GNIT has successfully organized a Three Day National Level Workshop on "Real time DSP System Design, Coding and Optimization using TMS3206713 DSK".

Around 30 participants from different parts of the country has participated in this 3 day National Level Hands-on Workshop. Resource person namely **Mr. Madhusudhan Rao**, an industry Expert who has a great experience working on DSP processors, delivered the sessions which laid a strong foundation of **TMS3206713 DSK** Programming.

Participants at the Workshop

Mr. Madhusudhan Rao, delivering the workshop

Participants Working on DSP boards....

Instructor Clarifying the Doubts of the Participants

Prof.B.Kedarnath, HOD-ECE and Wokshop chair ,   Mr. I.Sharath Chandra, Asst. Professor and Convenor of the Workshop addressing the participants

Giving away the Certificates to the participants....