



GURU NANAK INSTITUTE OF TECHNOLOGY

City Office: B2, 2nd Flr, Above Bata, Vikramপুরi Colony, Karkhana Road, Secunderabad-50009, Telangana, India.
Ph: +91-40-6632 3294, 6517 6117, Fax: +91-40-2789 2633
www.gnithyd.ac.in Campus: Ibrahimpatnam, R.R. District, Hyderabad-501506, Telangana, India. Ph: (0/95) 8414-20 21 20/21

DEPARTMENT OF ELECTRONICS AND COMMUNICATIONS ENGINEERING

Date: 4/6/2018

CIRCULAR

All the students of department of ECE are informed to express their interest by enrolling their name for the One week Certificate course on 'VLSI Design Technology using EDA simulation tools' starting from 2/7/2018 to 7/7/2018 .

The detailed syllabus for the course is attached for your information and the enrolled students will be certified accordingly based upon the test conducted at the end of the course.

Concerned Class In charges are instructed to submit the list of the students enrolled within two days to the undersigned. For further information, you can contact the Course Coordinator or the Head of the Department.

HOD ECE

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ECE Department
Guru Nanak Institute of Technology
Ibrahimpatnam, R.R. Dist-A.P

Copy to:

- Respected Principal and Vice Principal (for Information)
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

COURSE SYLLABUS FOR "VLSI Technology Design Using EDA Tools"

| S.No. | Day | Topics to be delivered |
|-------|-------|---|
| 1 | DAY 1 | MOS Circuit Design Process |
| 2 | DAY 2 | Logic Design Pass transistor and transmission gate – static CMOS design, Pseudo NMOS, and dynamic CMOS logic – Clocked CMOS logic – domino logic-Precharged domino logic, Dual rail logic with suitable examples. |
| 3 | DAY 3 | Sequential Logic Clocked sequential circuits – Two phase clocking – charge storage – dynamic sequential circuits – JK Flip-flop circuit, Memory Design-DRAM, SRAM and Flash Memory. |
| 4 | DAY 4 | Data path Subsystem Introduction, Design of Adders: carry look ahead - carry select - carry save, One/Zero Detector, Comparator-Magnitude, Equality, Counters-Binary Counter, LFSR, Parity generators. |
| 5 | DAY 5 | PLA design – Arithmetic logic unit design- Design of multipliers: Parallel Multipliers, Array, 2's Complement, |
| 6 | DAY 6 | Booth - Braun - Baugh - Wooley - Wallace tree, Dadda Multipliers, Serial Multiplication. |
| 7 | DAY 7 | Design and Simulation of VLSI circuits nMOS inverter- domino logic- two phase clocking-LFSR |

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DEPARTMENT OF ECE

LIST OF STUDENTS ENROLLED FOR VLSI DESIGN TECHNOLOGY USING EDA SIMULATION TOOLS ,2018-19

| SLNo. | Roll No. | Name of the student | Year | Dept. |
|-------|------------|------------------------------------|------|-------|
| 1 | 16831A0492 | S YASHASWI | III | ECE |
| 2 | 16831A0493 | SADUPALLY ANUSHA | III | ECE |
| 3 | 16831A0494 | SARAI BRAMARAMBIKA | III | ECE |
| 4 | 16831A0496 | SHAIK THAMEEM ANSARI | III | ECE |
| 5 | 16831A0497 | SHASHANK NATH | III | ECE |
| 6 | 16831A0498 | SRIRAM RAJA SRINIVAS | III | ECE |
| 7 | 16831A0499 | SRIVATS PATWARI | III | ECE |
| 8 | 16831A04A0 | SUNEER SINGH RANA | III | ECE |
| 9 | 16831A04A1 | SUNKE AKSHITHA | III | ECE |
| 10 | 16831A04A2 | SURAPANENI VAMSHIDHAR | III | ECE |
| 11 | 16831A04A3 | TULA JAYANTH RAO | III | ECE |
| 12 | 16831A04A4 | VADLAKONDA MOUNISH | III | ECE |
| 13 | 16831A04A5 | VALLEPU DHANESHWARI | III | ECE |
| 14 | 16831A04A6 | VANGA JASHWANTH REDDY | III | ECE |
| 15 | 16831A04A7 | VELDANDI KARTHIKEYA | III | ECE |
| 16 | 16831A04A8 | VELIDANDI RAVINDRA KUMAR | III | ECE |
| 17 | 16831A04A9 | VELUVOLU TRIGUN | III | ECE |
| 18 | 16831A04B0 | VIPPA SOWMYA | III | ECE |
| 19 | 16831A04B5 | YADAVILLI SWARNA GAYATRI PRATYUSHA | III | ECE |
| 20 | 16831A04B6 | BURRA ANUDEEP | III | ECE |
| 21 | 16831A04B7 | SYED SAJID HUSSAIN | III | ECE |
| 22 | 16831A04B8 | PABBA VAMSHI | III | ECE |
| 23 | 16831A04B9 | V MOHAN REDDY | III | ECE |
| 24 | 16831A04C0 | P AKHIL KUMAR | III | ECE |
| 25 | 17835A0413 | DANDOTHKAR HARSHITHA | III | ECE |
| 26 | 17835A0414 | GOGU MANASA | III | ECE |
| 27 | 17835A0415 | KUNIMARI SAI PRIYA | III | ECE |
| 28 | 17835A0417 | MUKKUGOGU PRAVEEN | III | ECE |
| 29 | 17835A0418 | NAYINI JAGADEESH | III | ECE |
| 30 | 17835A0419 | NUNE MANIKANTA | III | ECE |
| 31 | 17835A0420 | ONTEDDU SHIVA KUMAR | III | ECE |
| 32 | 17835A0421 | P APURVA | III | ECE |
| 33 | 17835A0403 | CHELMELLA SAMPATH | III | ECE |
| 34 | 17835A0404 | CHINTHANIPPULA YESHWANTH | III | ECE |
| 35 | 17835A0405 | DORLA VENKATESH REDDY | III | ECE |
| 36 | 17835A0406 | DOTI PARAMESHWARI | III | ECE |

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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

MARKS SHEET FOR VLSI TECHNOLOGY DESIGN USING EDA TOOLS

| S.no | Hallticket no | Name of the student | Year | Branch | MARKS |
|------|---------------|--------------------------|------|--------|-------|
| 1 | 16831A0492 | S YASHASWI | III | ECE | 10 |
| 2 | 16831A0493 | SADUPALLY ANUSHA | III | ECE | 10 |
| 3 | 16831A0494 | SARAI BRAMARAMBIKA | III | ECE | 9 |
| 4 | 16831A0496 | SHAIK THAMEEM ANSARI | III | ECE | 10 |
| 5 | 16831A0497 | SHASHANK NATH | III | ECE | 10 |
| 6 | 16831A0498 | SRIRAM RAJA SRINIVAS | III | ECE | 9 |
| 7 | 16831A0499 | SRIVATS PATWARI | III | ECE | 9 |
| 8 | 16831A04A0 | SUMEER SINGH RANA | III | ECE | 9 |
| 9 | 16831A04A1 | SUNKE AKSHITHA | III | ECE | 10 |
| 10 | 16831A04A2 | SURAPANENI VAMSHIDHAR | III | ECE | 10 |
| 11 | 16831A04A3 | TULA JAYANTH RAO | III | ECE | 10 |
| 12 | 16831A04A4 | VADLAKONDA MOUNISH | III | ECE | 9 |
| 13 | 16831A04A5 | VALLEPU DHANESHWARI | III | ECE | 9 |
| 14 | 16831A04A6 | VANGA JASHWANTH REDDY | III | ECE | 10 |
| 15 | 16831A04A7 | VELDANDI KARTHIKEYA | III | ECE | 10 |
| 16 | 16831A04A8 | VELIDANDI RAVINDRA KUMAR | III | ECE | 10 |
| 17 | 16831A04A9 | VELUVOLU TRIGUN | III | ECE | 9 |
| 18 | 16831A04B0 | VIPPA SOWMYA | III | ECE | 10 |
| 19 | 16831A04B5 | SWARNA GAYATRI PRATYUSHA | III | ECE | 10 |
| 20 | 16831A04B6 | BURRA ANUDEEP | III | ECE | 10 |
| 21 | 16831A04B7 | SYED SAJID HUSSAIN | III | ECE | 10 |
| 22 | 16831A04B8 | PABBA VAMSHI | III | ECE | 10 |
| 23 | 16831A04B9 | V MOHAN REDDY | III | ECE | 7 |
| 24 | 16831A04C0 | P AKHIL KUMAR | III | ECE | 10 |
| 25 | 17835A0413 | DANDOTHKAR HARSHITHA | III | ECE | 10 |
| 26 | 17835A0414 | GOGU MANASA | III | ECE | 10 |
| 27 | 17835A0415 | KUMMARI SAI PRIYA | III | ECE | 9 |
| 28 | 17835A0417 | MUKKUGOGU PRAVEEN | III | ECE | 9 |
| 29 | 17835A0418 | NAYINI JAGADEESH | III | ECE | 10 |
| 30 | 17835A0419 | NUNE MANIKANTA | III | ECE | 10 |
| 31 | 17835A0420 | ONTEDDU SHIVA KUMAR | III | ECE | 10 |
| 32 | 17835A0421 | P APURVA | III | ECE | 10 |



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| | | | | | |
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| 33 | 17835A0403 | CHELIMELLA SAMPATH | III | ECE | 10 |
| 34 | 17835A0404 | CHINTHANIPPULA YESHWANTH | III | ECE | 9 |
| 35 | 17835A0405 | DORLA VENKATESH REDDY | III | ECE | 10 |
| 36 | 17835A0406 | DOTI PARAMESHWARI | III | ECE | 10 |

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DEPARTMENT OF ELCTRONICS AND COMMUNICATIONS ENGINEERING

REPORT ON ONE WEEK CERTIFICATE COURSE ON 'VLSI DESIGN TECHNOLOGY USING EDA SIMULATION TOOLS'

Date: 14/07/ 2018

The Course entitled One week Certificate course on 'VLSI Design Technology using EDA simulation tools' has been successfully conducted from 02/07/2018 to 07/07/2018 in the department of ECE, GNIT. The total number of students who have successfully completed the course are 36 .

After the successful completion of the course, the students are able to achieve the following outcomes:

- Knowledge on VLSI Design and Manufacturing process
- Able to use Design Kit for Design and Verification
- Complete idea on VLSI Design Methodologies
- Ability to do Research/Academic Projects
- Hands-on Practice over Industry Standard EDA Tools

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Department of Electronics & Communication Engineering

ONE WEEK CERTIFICATE COURSE ON


"VLSI Design Technology using EDA Simulation Tools"

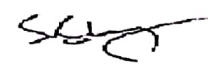
(02-07-2018 to 07-07-2018)

CERTIFICATE

This is to certify that Ms. V. Sowmya of III ECE
GNIT

has successfully completed the
One Week Certificate course on "VLSI Design Technology using EDA Simulation Tools"
at Guru Nanak Institute of Technology, Hyderabad.


Dr Ravi Shankar Mishra
HOD ECE


Dr S Sreenatha Reddy
Principal

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